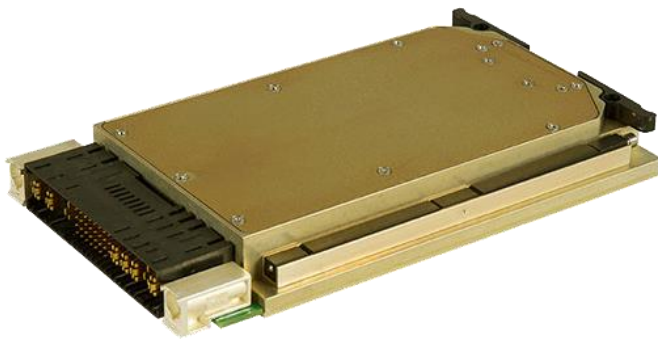


M4162 SERIES

DC/DC HOLDUP UNIT



PRODUCT HIGHLIGHTS

- VITA 62 COMPLIANT
- 3U FORM FACTOR
- WIDE INPUT RANGE
- IPMI / 46.11 COMMUNICATION
- UP to 48J

M4162 SERIES VPX DC/DC HOLDUP UNIT

<h3>Electrical Specifications</h3> <table style="width: 100%; border: none;"> <tr> <td style="width: 33%; vertical-align: top; padding: 5px;"> <p><u>DC Input</u> Up to 100V_{DC} Continuous work during MIL-STD-704 transients</p> <p><u>Communication</u> IPMI / 46.11 protocol available for voltages</p> </td> <td style="width: 33%; vertical-align: top; padding: 5px;"> <p><u>DC Output</u> VS1: Power VS2: Power return Normally: Input Follower During Power loss: Output is Up to 85V</p> <p><u>Efficiency</u> Typical 98%</p> </td> <td style="width: 33%; vertical-align: top; padding: 5px;"> <p><u>Isolation</u> Over 20 MΩ at test voltage: 200V between Input & output to case</p> <p><u>EMC</u> Complies with MIL-STD-461F (5μH LISN): CE101, CE102, CS101</p> </td> </tr> </table>			<p><u>DC Input</u> Up to 100V_{DC} Continuous work during MIL-STD-704 transients</p> <p><u>Communication</u> IPMI / 46.11 protocol available for voltages</p>	<p><u>DC Output</u> VS1: Power VS2: Power return Normally: Input Follower During Power loss: Output is Up to 85V</p> <p><u>Efficiency</u> Typical 98%</p>	<p><u>Isolation</u> Over 20 MΩ at test voltage: 200V between Input & output to case</p> <p><u>EMC</u> Complies with MIL-STD-461F (5μH LISN): CE101, CE102, CS101</p>
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M4162 SERIES VPX DC/DC HOLDUP UNIT

Normal Operation: During Normal Operation, the M4162 works as an Input follower. Small Voltage drop, of less than 1V is expected

Hold Up Operation: When Voltage at the input of the unit, drops below 18V, the Holdup will discharge it's internal capacitance into the DC Bus. During this time the M4162 output voltage will rise up to 85V and will decrease as the capacitor bank energy will be discharging.

Recharging of the capacitor bank will start before Input voltage goes back to steady state line.

The M4162 charging time is less than 0.5Sec per Mil-STD-704, during this time, the average charging current taken from the source can be calculated as follow

$$E = \frac{C * Vc^2}{2}$$

$$I = \frac{E}{Vin * t * Eff}$$

Where **E** is Holdup Energy, **Vc** and **C** are the Holdup capacitance and charging voltage, **Vin** is input voltage and **Eff** is the charging circuit efficiency.

For specific details contact Factory.

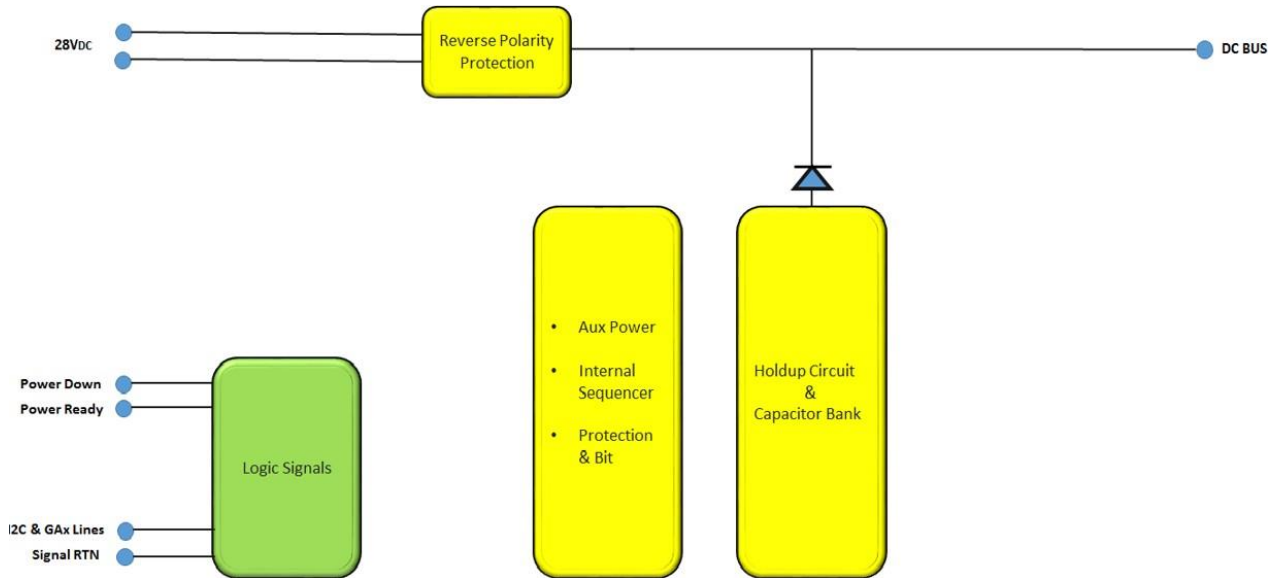
Reducing Charging current and increasing charge time is optional.

Functions and Signals - According to VITA 62

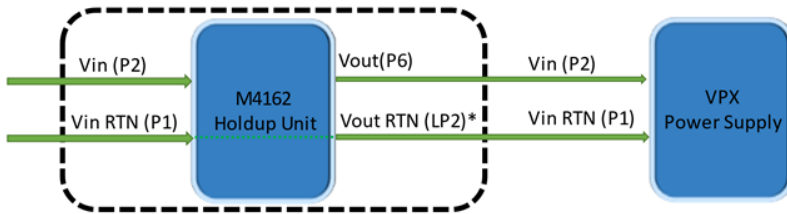
Signal No.	Signal Name	Type	Description	Pin No'
1	Power Down	Output	Indicates that Holdup event has occurred. Open Drain. Normally Open, goes low during Holdup time.	D1
2	Power Ready	Output	Indicates to other modules that Holdup capacitor bank is Fully charged. Open Drain. Normally Open, goes low when Holdup energy under 90%.	A3
5	GA0, GA1	Input	Used for geographical addressing. GA1 is the most significant bit and GA0 is the least significant bit.	A5,B5
6	SCL, SDA	Bidirectional	I2C bus Clock and Data respectively. Through this bus the voltage and temperature readouts can be shared.	C5,D5

M4162 SERIES VPX DC/DC HOLDUP UNIT

Simplified Block Diagram



Typical Application



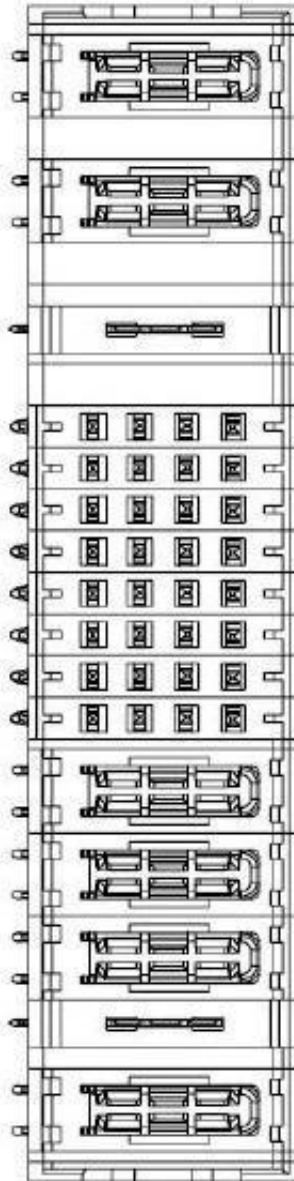
- *
- Vin RTN & Vout RTN are internally shorted
 - When Input Current exceed 20A, P5 should be added as Vout RTN line
 - Since Hold-up event generate ground noise, it is important to keep Power Supply Input to Output grounds isolation
 - All signals are floating and can be referred to Power Supply Signal ground

M4162 SERIES VPX DC/DC HOLDUP UNIT

Pin Assignment

Pin Number	Pin Name
P1	-DC_IN
P2	+DC_IN
LP1	CHASSIS
P3	N.C.
P4	N.C.
P5	N.C.
LP2	Bus_RTN
P6	Bus
A8	N.C.
B8	N.C.
C8	N.C.
D8	N.C.
A7	N.C.
B7	N.C.
C7	N.C.
D7	Sig_RTN
A6	SCL_B
B6	SDA_B
C6	N.C.
D6	N.C.
A5	GA0*
B5	GA1*
C5	SCL_A
D5	SDA_A
A4	N.C.
B4	N.C.
C4	N.C.
D4	N.C.
A3	Power_Ready
B3	N.C.
C3	N.C.
D3	N.C.
A2	N.C.
B2	N.C.
C2	N.C.
D2	N.C.
A1	N.C.
B1	N.C.
C1	N.C.
D1	Power_Down

M4162 SERIES VPX DC/DC HOLDUP UNIT

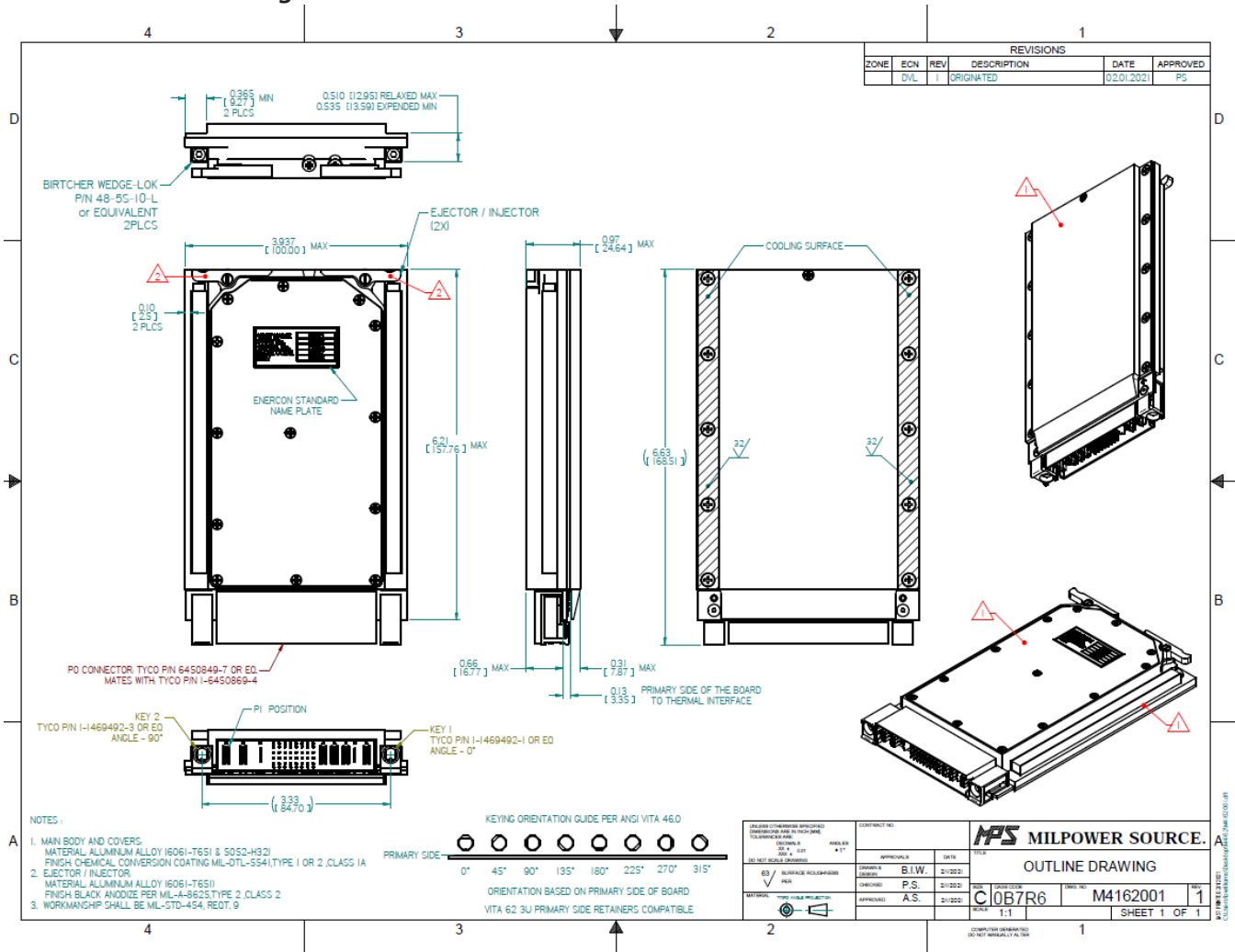


2ACP+1LP+32S+3HDP+1LP+1HDP

PART NUMBER	ROWS	POWER			SIGNAL								POWER					
		P1	P2	LP1	1	2	3	4	5	6	7	8	P3	P4	P5	LP2	P6	
6450849-7	D				Z5	Z5	Z5	Z5	Z5	Z5	Z5	Z5						
	C	TT	TT	LT	Y5	Y5	Y5	Y5	Y5	Y5	Y5	Y5	TT	TT	TT	LT	TT	
	B				R5	R5	R5	R5	R5	R5	R5	R5						
	A				Q5	Q5	Q5	Q5	Q5	Q5	Q5	Q5	Q5	Q1				

M4162 SERIES VPX DC/DC HOLDUP UNIT

Outline Drawing



Notes:

1. Dimensions are in inches [mm]
2. Tolerance is:
.XX ± 0.02 IN
.XXX ± 0.008 IN
3. Weight: Approx. TBD
4. 3D model available

Note: Specifications are subject to change without prior notice by the manufacturer.